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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/841,644	04/30/1997	SHUNPEI YAMAZAKI	0756-1603	3399
7590 03/21/2006			EXAMINER	
Eric J. Robinson			SCHILLINGER, LAURA M	
Robinson Intellectual Property Law Office 21010 Southbank Street			ART UNIT	PAPER NUMBER
PMB 955			2813	
Potomac Falls,	VA 20165	DATE MAIL ED. 02/21/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

-4	
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	Application No.	Applicant(s)			
Office Action Commons	08/841,644	YAMAZAKI ET AL.			
Office Action Summary	Examiner	Art Unit			
	Laura M. Schillinger	2813			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim 11 apply and will expire SIX (6) MONTHS from cause the application to become ABANDONET	l. ely filed the mailing date of this communication. O (35 U.S.C. § 133).			
Status					
1)⊠ Responsive to communication(s) filed on 14 De	ecember 2005.				
3) Since this application is in condition for allowan					
closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4) Claim(s) 23-32 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 23-32 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.					
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the original than the correction of the correction of the original than the correction of the correction o	epted or b) objected to by the Edrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ☒ All b) ☐ Some * c) ☐ None of: 1. ☒ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s)					
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>9/2/05</u>. 	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				
S. Patent and Trademark Office					

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DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 23-32 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Applicant filed a response to demonstrate support for claim 23 which includes a chart mapping support for the claims within the specification.

However, the chart still fails to show "simultaneously forming an overlying gate insulator on a top and sidewalls of the gate electrode <u>and said gate line</u> by anodic oxidation of said gate electrode and <u>said gate line</u> to reduce the dimensions of said gate electrode and <u>said gate line</u>.., as required by Applicant's claim.

The Examiner carefully reviewed the portions of the specification pointed out by the Applicant to find support for the language of the claim, however found no such support.

Applicant's specification on pages 5, lines: 24 through page 6, line:13; Page 6, lines: 22-Page 7, line:4; Page 21, lines:19-24; Page 22, lines:23-30, in addition to Fig.s 1, 3E and 3G all demonstrate anodic oxidation of only the gate electrode and does not teach nor suggest anodic

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oxidation of the gate line. Applicant's specification fails to teach anodic oxidation of the gate line at all, let alone doing so simultaneously with the gate electrode.

In order for a simultaneous anodic oxidation to occur between the gate electrode and gate line, the gate line must be exposed to the anodic oxidation fluid along with the gate electrode. It cannot be blocked by another layer because such a layer would prevent the gate line from oxidizing. There is no indication from Applicant's specification that the gate line is exposed during oxidation. There is no showing of whether the gate line is formed beneath or above the gate electrode.

Moreover, the specification suggests that the gate line is separate from the gate electrode and therefore not patterned from the same layer. On pages 38-40 of Applicant's specification, there is a detailed teaching for Fig.s 11A-11C which demonstrate of the formation of the gate electrode and its subsequent anodic oxidation. This passage suggests that the Applicant's method would not simultaneously oxidize the gate electrode and line. The specification teaches that the gate electrode material is formed, masked, and patterned to form an electrode structure. The specification teaches that after formation of the gate electrode, the gate structure is then subsequently connected to a gate wire 157. Therefore the specification teaches gate electrode is not the same layer as the gate wire; moreover it suggests that the gate wire is formed prior to the gate electrode. To further complicate matters, gate wire 157 is described in the specification, however not shown in Fig.s 11A-11C.

Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Laura M. Schillinger whose telephone number is (571) 272-1697. The examiner can normally be reached on M-T, R-F 7:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

03/05/06

Laura M Schillinger Primary Examiner Art Unit 2813